

Name of the Faculty	Dr. Dattesh Yogeshkumar Joshi
Designation	Assistant Professor
Email id	datteshjoshi.ee@bitseducampus.ac.in
Contact number	9904981945
Total Experience	9 Years & 8 Months
Teaching (years, months)	8 Years & 8 Months
Industry (years, months)	1 Year
Professional Summary	<ul style="list-style-type: none"> • Working as an Assistant Professor in Electrical Engineering Department of BIT, • Worked as a Graduate Engineer Trainee (GET) at M/s Eliokem India Pvt. Ltd (At –Village-Dungri, Taluka-Valia, Dist.-Bharuch, Gujarat.) • Associate Member of Institution of Engineers (India), • Member of International Association of Engineers (IAENG)
Current Activities	<ul style="list-style-type: none"> • Department Project Committee Coordinator, • Department NBA Committee Co-Coordinator, • Hostel Committee Member (SDBS Boys Hostel)-BITS Edu Campus
Specialization Areas	<ul style="list-style-type: none"> • Distributed Generation System, • Electrical Machines, • Electrical Machine Design
Subject Taught (UG/PG)	<ul style="list-style-type: none"> • Testing & Commissioning of Electrical Equipments, • Design of AC Machines, • Design of DC Machines & Transformer, • DC Machines & Transformer, • AC Machines
Qualifications	<p>Ph. D in Electrical Engineering (2017) Pacific University- Udaipur, Rajasthan</p> <p>M. Tech (Electrical Engineering) (2010) Sardar Vallabhbhai National Institute of Technology, Surat.</p> <p>B.E. (Electrical) (2007) Veer Narmad South Gujarat University, Surat.</p>
Skills	Proficiency in Matlab, Lab-View simulation software
Achievements	<ul style="list-style-type: none"> ▪ Secured 1059 All India ranks in GATE-2008 with 93.09 percentile. (Graduate Aptitude Test in Engineering), ▪ Appreciation for Best paper presented in All India Seminar on Energy Management and Audit Organized by The Institution of India (India).

- Gujarat Technological University Innovation Sankul Vadodara; 1, In recognition of the distinguished service rendered, Department of Electrical Engineering, BITS Edu Campus, Varnama, Vadodara is adjudged Best Project Guide for the Year 2012.
- A guided project at UG level selected in Top 65 Innovations at the “Switch Global Expo-2016” during 6th Oct. to 10th Oct., 2016.
- Guided Project selected to exhibit in the innovation corner at “Vibrant Gujarat Start-up Summit 2016”.

Research Project Publications

Nil

1. Dynamic Modelling, Analysis and Simulation of Distributed Generation System with FACTS Devices-SVC & STATCOM, Discovery The International Daily Journal, 21 October 2015, Page No. 32-37, ISSN No.-2278-5469, EISSN 2278-5450.
2. Power Quality of Grid Connected Distributed Generation System: Role of FACTS Device, International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, Vol.3, Issue 2, February 2015, ISSN (Online) 2321-2004, ISSN (Print) 2321-5526, DOI 10.17148/IJIREEEICE.2015.3213, Page No. 50-53.
3. New Method for Islanding Detection of Distributed Generation System, International Journal of Advance Research in Engineering, Science & Technology, Vol. 3, Issue 1, January 2016, e-ISSN: 2393-9877, p-ISSN: 2394-2444.
4. Voltage Stability & Power Quality Assessment of Distributed Generation Based Wind Farm System, International Journal for Scientific Research & Development, Vol. 1, Issue 5, 2013, ISSN (Online): 2321-0613, Page No. 1073-1076.
5. Dynamic Modelling, Analysis and Simulation of Distributed Generation System with FACTS Devices- SSSC & UPFC, International Journal of Advance Engineering and Research Development, Vol. 4, Issue 6, June-2017, e-ISSN (O): 2348-4470, p-ISSN (P): 2348-6406.
6. Role of FACTS Device with Distributed Generation System, JASC: Journal of Applied Science and Computation, UGC Approved, Volume 5, Issue 11, November 2018, ISSN NO: 1076-5131, DOI: 16.10089, Page No: 652.
7. Energy Management of Distributed Generation in Micro Grid System, JASC: Journal of Applied Science and Computation, UGC Approved, Volume 5, Issue 11, November 2018, ISSN NO: 1076-5131, DOI: 16.10089, Page No: 641.
8. Electrical Fault Detection in Three Phase Induction Motor by Vibration Analysis using Piezoelectric Accelerometer, 1st International Conference on Current Trends in Technology, NUiCONE 2010, Nirma University, Ahmedabad, Gujarat, 09-11 December, 2010.
9. Power Quality Improvement using Static Compensator on Grid Integration of Wind Energy System, 2nd IEEE, International Conference on Current Trends in Technology- NUiCONE 2011, Nirma University, Ahmedabad, Gujarat, 08-10 December, 2011.
10. Power Loss Decomposition Technique in Deregulated Electrical Market for Effective Power System Operation, 2018 IEEE International Conference on Current Trends towards Converging Technologies, SVS College of

Engineering, Coimbatore, Tamil Nadu, India, 01-03, March 2018.

11. Energy Management in Distributed Generation, All India Seminar on Energy Management and Audit Organized by The Institution of India (India), Gujarat State Centre In Association with Birla Vishvakarama Maha Vidhyalaya, V.V.Nagar, 26-27 October, 2018.
12. Distributing Electrical Energy Efficiently and Economically in Rural India: The Micro-Grid Option, Power systems, Embedded systems, Power electronics, Communication, Control and Instrumentation- PEPCCI-2014, 23-25 January-2014, Page No. 153-157, ISBN 978-81-923462-2-9.
13. The Contribution of Wind Energy for Green Energy & Environment in India, Power systems, Embedded systems, Power electronics, Communication, Control and Instrumentation- PEPCCI-2013, 28th to 30th Jan.-2013.
14. Overview of Transmission Line Loss Separation between Generation and Load for Restructuring Electrical Industries, Power systems, Embedded systems, Power electronics, Communication, Control and Instrumentation- PEPCCI-2013, 28th to 30th Jan.-2013.
15. Operation of D-STATCOM for Voltage Control in Distribution Networks with a New Control Strategy, Recent Trends in Engineering and Technology, BVM College, V.V. Nagar, 13th & 14th May 2011.

Activities and honours

- Reviewer in International Journal of Creative Research Thoughts-IJCRT, international peer reviewed Journal.
- Attended as a Project Team Mentor in “Gujarat Industrial Hackathon 2018-19”, organized by Higher and Technical Education Department, Government of Gujarat, Hosted at AURO University, Surat, Gujarat on 19th & 20th February 2019.
- Attended two days seminar on “Recent Trends in Power Electronics & Embedded Systems” at Babaria Institute of Technology, BITS Edu Campus on 4th & 5th January 2019.
- Performed the duty of Technical committee Co-ordinator in GTU Tech-Fest-2018 in association with BITS Edu Campus.
- Attended one week STTP on “Power System Modelling, Analysis and Control” at G.H.Patel College of Engineering & Technology, V.V.Nagar from 10th to 15th December 2018.
- Attended All India Seminar on “Energy Management and Audit” at The Institution of India (India), Gujarat State Centre In Association with Birla Vishvakarama Mahavidhyalaya at BVM, V. V. Nagar, on 26th & 27th October, 2018.
- Attended one day workshop on “Internet of Things” at Babaria Institute of Technology, BITS Edu Campus on 25th March 2018.
- Reviewer of “All India Seminar on Recent Trends in Renewable Energy Application & Research”, Organized by Gujarat State Centre, The Institute of

Engineers (INDIA), February 16-17, 2018.

- Attended two days seminar on “Recent Trends in Renewable Energy Application & Research”, Organized by The Institution of Engineers (India), Gujarat State Centre at Ahmedabad on 16th & 17th February 2018.
- Performed the duty of Registration Committee Co-ordinator of “KHOJ-2017”- A State Level Science Exhibition & Competition at BITS Edu Campus, 11th February, 2017.
- Skill Enhancement Training on “Overview and Switching Operations of Switchyard Equipments” at 66kV GIS & AIS Substation- Halol Project (Project executed by SIEMENS from 6th June to 11th June, 2016).
- Performed the duty of Nodal Officer for BITS Edu Campus for Switch Global Expo-2016.
- Attended two day workshop on “Graphical System Design in Engineering Applications”, at Babaria Institute of Technology on 15th & 16th July 2015.
- Attended one day workshop on “IEEE Xplore Digital Library Awareness Workshop”, at Babaria Institute of Technology on 7th August 2015.
- Attended one day workshop on “Linux Workshop”, Organized by BIT-OSTC at BITS Edu Campus on 13th February 2014.
- Attended one day seminar on “Eradicating Classroom Boundaries in Effective Teaching (High Tech _ Low Tech _ No Tech)”, Organized by S & H Department, Babaria Institute of Technology, BITS Edu Campus on 2nd November 2014.
- Attended one day seminar on “Developing Student Friendly Approach among the Teachers with ICT”, Organized by S & H Department, Babaria Institute of Technology, BITS Edu Campus on 4th July November 2013.
- Attended One day workshop on “Patent Search Methodology” at BITS Edu Campus on 20/10/2012.
- Co-ordinator of Short Term Training Program on “Grid Connected Wind Farm System” sponsored by CEPT, Organized by Electrical Engineering Department, Babaria Institute of Technology, BITS Edu Campus, 9th-12th January, 2012.
- Attended 2 days seminar on “Reactive Power Management” at FGI, Vadodara, 2th

& 3rd December, 2011.

- Attended Two week training on “Programmable Logic Controllers (Allen Bradley)” at Vijaya Technical Training Institute, Vadodara from 1/1/2007 to 12/1/2007.

Grant Received

Nil